

**OFFICE OF THE SECRETARY
MEGHALAYA STATE COUNCIL FOR TECHNICAL EDUCATION
SHILLONG**

No: MSCTE/Routine/Odd.Ex/SP/2024/3

Dated Shillong the 30th October 2024.

**PROGRAMME FOR FIFTH SEMESTER (REGULAR & BACKLOG)
FINAL DIPLOMA EXAMINATION IN ENGINEERING 2024**

SHILLONG POLYTECHNIC CENTRE

**CIVIL (CE), ELECTRICAL (EE), MECHANICAL (ME),
ELECTRONICS & COMMUNICATION (ECE)/ COMPUTER SCIENCE & ENGINEERING (CS&E)**

| Date & Day | Branch | Subject | Time |
|---------------------------|--|---|-----------------|
| 28.11.2024 (Thursday) | Civil Engineering (Regular & Backlog) | Water Supply and Sanitary Engineering (CE509) | 1:00 PM-4:00 PM |
| | Electrical, Mechanical Engineering (Regular & Backlog) | Entrepreneurship Development (G304) | 1:00 PM-4:00 PM |
| | Electronics & Communication, Electronics Engineering (Regular & Backlog) | Consumer Electronics (ECE505) | 1:00 PM-4:00 PM |
| | Computer Science & Engineering (Regular & Backlog) | Microprocessor & Interfacing (CSE406) | 1:00 PM-4:00 PM |
| 02.12.2024 (Monday) | Civil Engineering (Regular & Backlog) | Estimating-II (CE505) | 1:00 PM-4:00 PM |
| | Electrical Engineering (Regular & Backlog) | Electrical Machine-II (EE404) | 1:00 PM-4:00 PM |
| | Mechanical Engineering (Regular & Backlog) | Machine Tools-I (ME501) | 1:00 PM-4:00 PM |
| | Electronics & Communication, Electronics Engineering (Regular & Backlog) | Communication Engineering-II (ECE404) | 1:00 PM-4:00 PM |
| | Computer Science & Engineering (Regular & Backlog) | Computer Graphics & Multimedia (CSE410) | 1:00 PM-4:00 PM |
| 04.12.2024 (Wednesday) | Civil Engineering (Regular & Backlog) | Geo-Technical Engineering-I (CE506) | 1:00 PM-4:00 PM |
| | Electrical Engineering (Regular & Backlog) | Electrical Power System-II (EE406) | 1:00 PM-4:00 PM |
| | Mechanical Engineering (Regular & Backlog) | Machine Design (ME506) | 1:00 PM-4:00 PM |
| | Electronics & Communication Engineering (Regular & Backlog) | Microwave Technique (ECE506) | 1:00 PM-4:00 PM |
| | Computer Science & Engineering (Regular & Backlog) | Object Oriented Methodology (CSE502) | 1:00 PM-4:00 PM |
| 06.12.2024 (Friday) | Civil Engineering (Regular & Backlog) | Highway Engineering (CE510) | 1:00 PM-4:00 PM |
| | Electrical, Electronics & Communication Engineering (Regular & Backlog) | Digital Electronics & Microprocessor-II (EE504, ECE504) | 1:00 PM-4:00 PM |
| | Mechanical Engineering (Regular & Backlog) | Production Management (ME505) | 1:00 PM-4:00 PM |
| | Computer Science & Engineering (Regular & Backlog) | System Programming (CSE504) | 1:00 PM-4:00 PM |

| Date & Day | Branch | Subject | Time |
|-------------------------|--|--|-----------------|
| 10.12.2024 (Tuesday) | Civil Engineering (Regular & Backlog) | Theory of Structure (CE409) | 1:00 PM-4:00 PM |
| | Mechanical Engineering (Regular & Backlog) | Fabrication Technology (ME605) | 1:00 PM-4:00 PM |
| | Electronics & Communication Engineering (Regular & Backlog) | Biomedical Instrumentation (ECE604) | 1:00 PM-4:00 PM |
| | Computer Science & Engineering (Regular & Backlog) | Software Engineering (CSE506) | 1:00 PM-4:00 PM |
| 13.12.2024 (Friday) | Civil Engineering (Regular & Backlog) | Design & Detailing-II (CE502) | 1:00 PM-4:00 PM |
| | Electrical Engineering (Regular & Backlog) | Non Conventional Sources of Energy (EE604) | 1:00 PM-4:00 PM |
| | Mechanical Engineering (Regular & Backlog) | Automobile Engineering (ME504) | 1:00 PM-4:00 PM |
| | Electronics & Communication, Electronics Engineering (Regular & Backlog) | Electronics Measurement (ECE406) | 1:00 PM-4:00 PM |
| | Computer Science & Engineering (Regular & Backlog) | Data Base Management System (CSE501) | 1:00 PM-4:00 PM |

N.B. The date and timing for Practical Examinations & Viva Voce Examinations for all the subjects as per curriculum and syllabus held and notified by the Institute Authority, a copy of the same is to be forwarded to the Secretary, Meghalaya State Council for Technical Education (MSCTE)

Sd/-
Secretary
Meghalaya State Council for Technical Education
Shillong